

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	Methodology for Verifying Multi-Cycle and Clock-Domain-Crossing Logic Using Random Flip-Flop Delays
Application Number :	
Date :	
First Named Applicant:	Hin-Kwai Lee
Confirmation Number:	
Attorney Docket Number:	NM-103

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Documents being submitted:	Files
us-ids	NM103ex-usidst.xml us-ids.dtd us-ids.xsl
us-request	NM103ex-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	NM103ex-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	OathNM103e.tif
application-body	NM103e-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogrk3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd fgNM103e1.TIF fgNM103e2.TIF fgNM103e3.TIF fgNM103e4.TIF fgNM103e5.TIF fgNM103e6.TIF fgNM103e7.TIF fgNM103e8.TIF
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